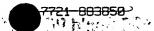
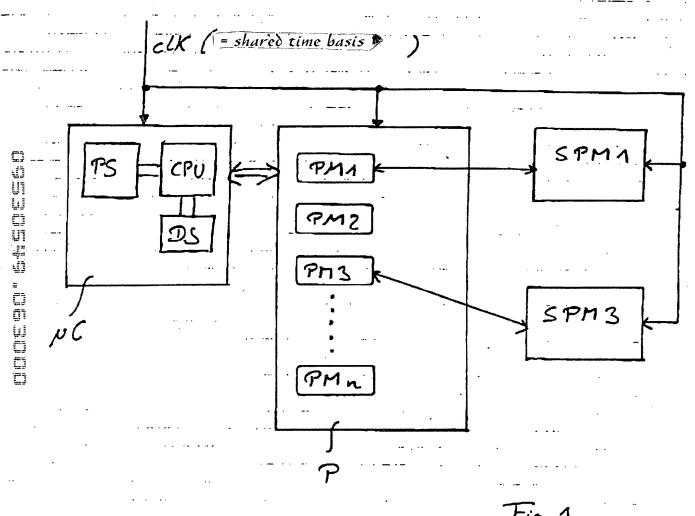


Fig. 1

ADC





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start of conversion NOP (else if Acts)
now seeme) 14 A> 17 Mov E800R, Mov ADE RUW, (25 Mark START wait MOV A
ADC RES - Jangdspylin Wait -- mode for test evaluation finished conversion & DOD 2 start Wait NOP Wait timing clocks? ADC simulated / CPU